Figure 1

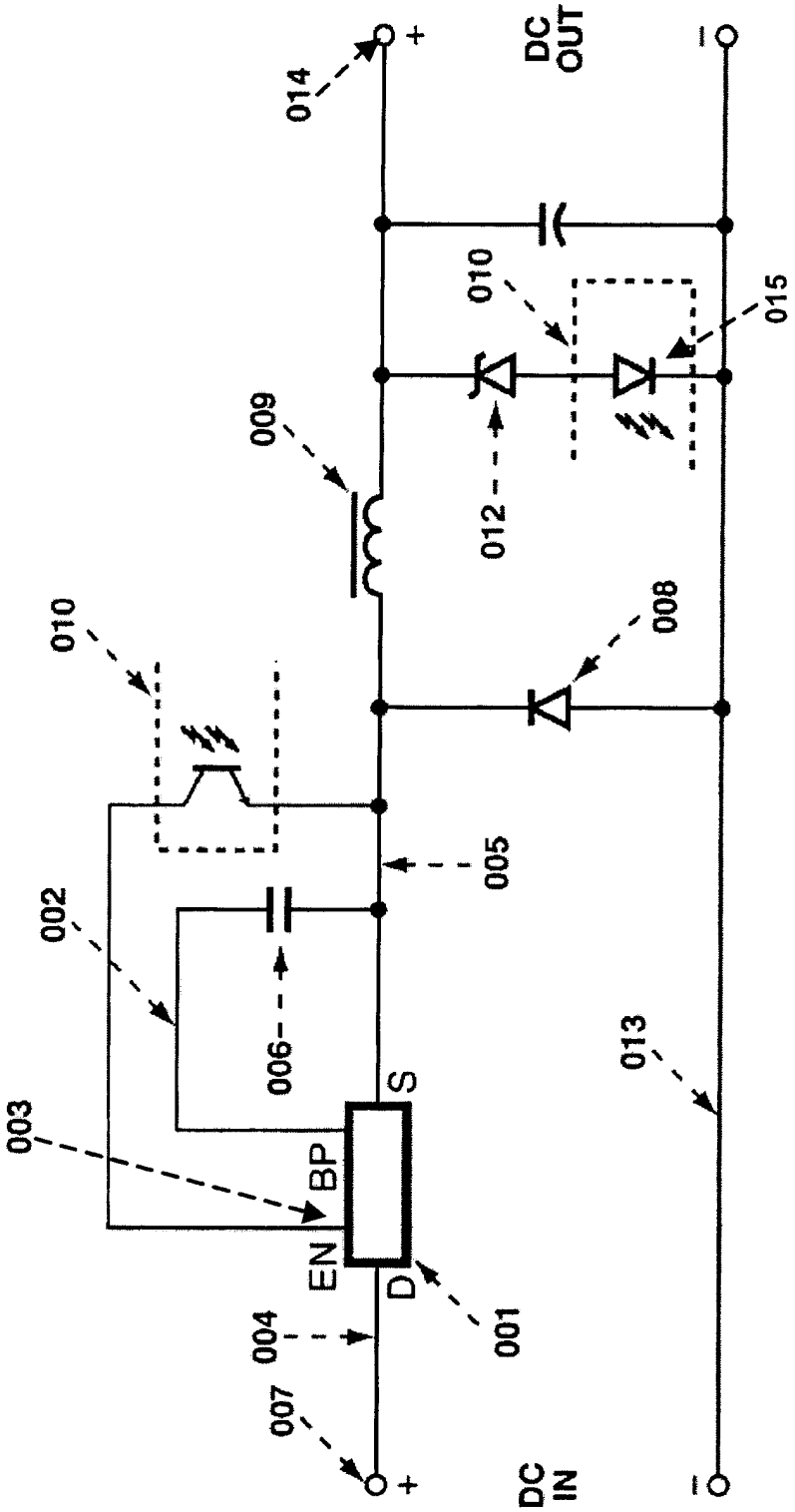

DC OUT
DC IN
+
−
014
010
015
009
012
008
005
002
006
003
BP
EN
S
D
001
004
007
013

Figure 2

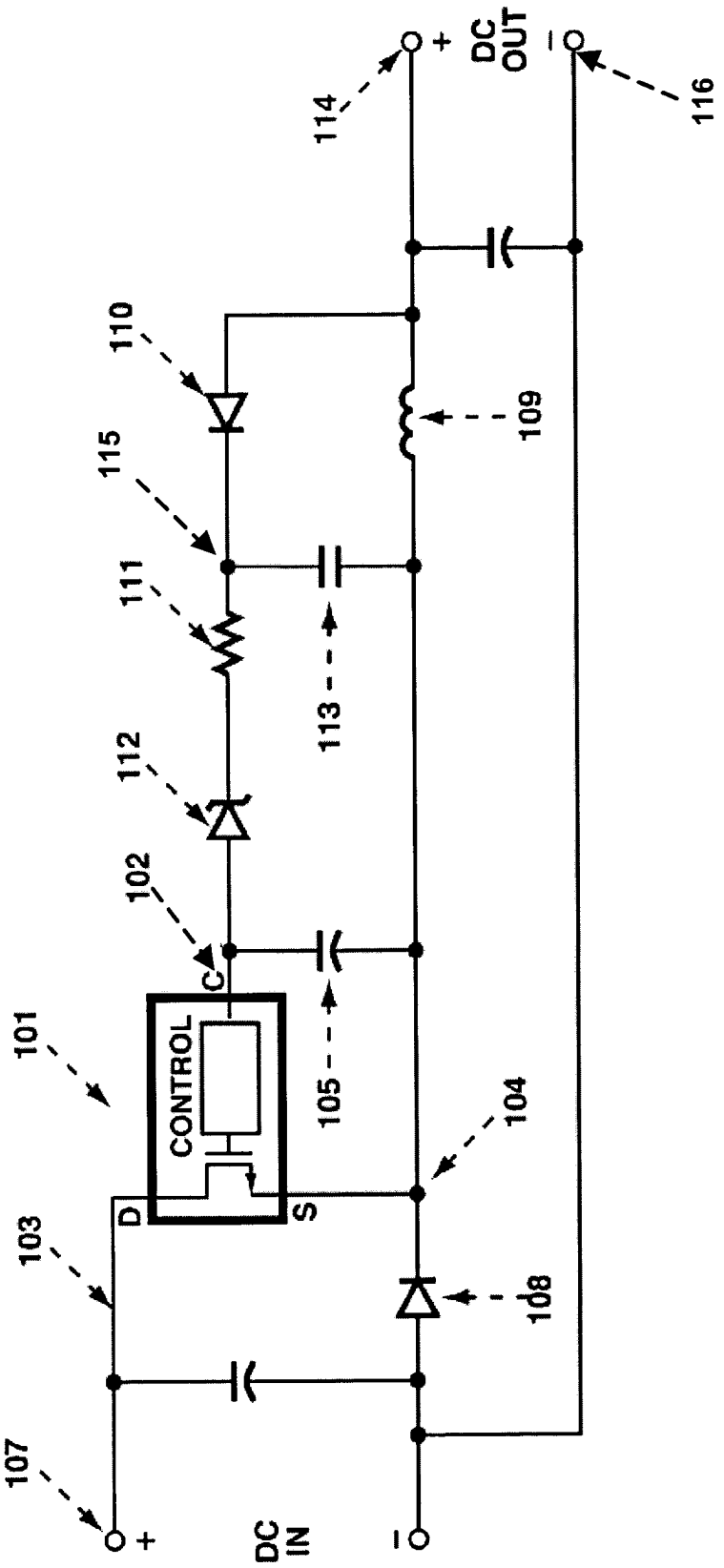

DC IN
+
-
DC OUT
+
-
CONTROL
C
D
S
101
102
103
104
105
107
108
109
110
111
112
113
114
115
116

Figure 3

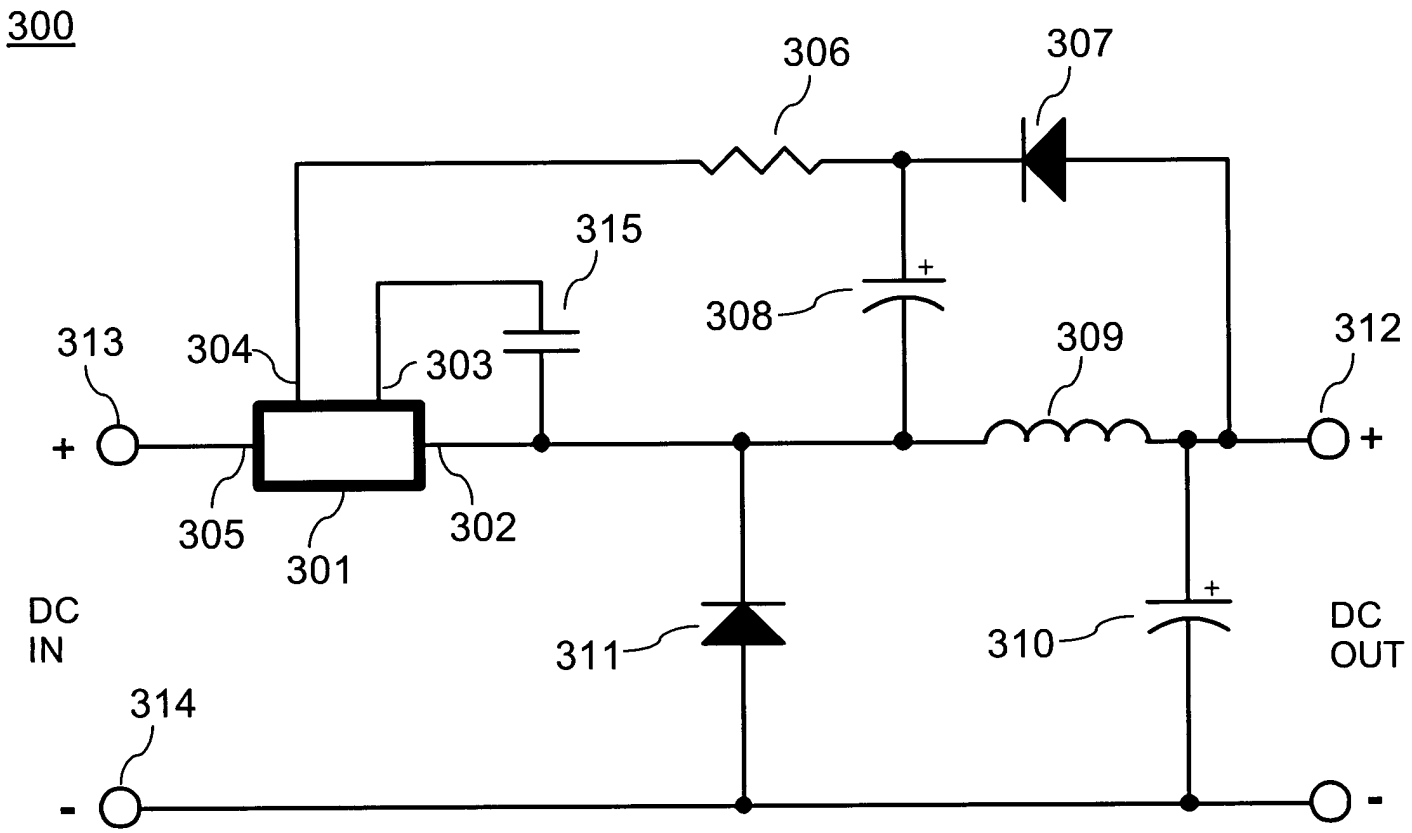

300
306
307
315
308
313
304
303
309
312
+
+
305
301
302
DC
IN
311
310
DC
OUT
314
-
-

Figure 4

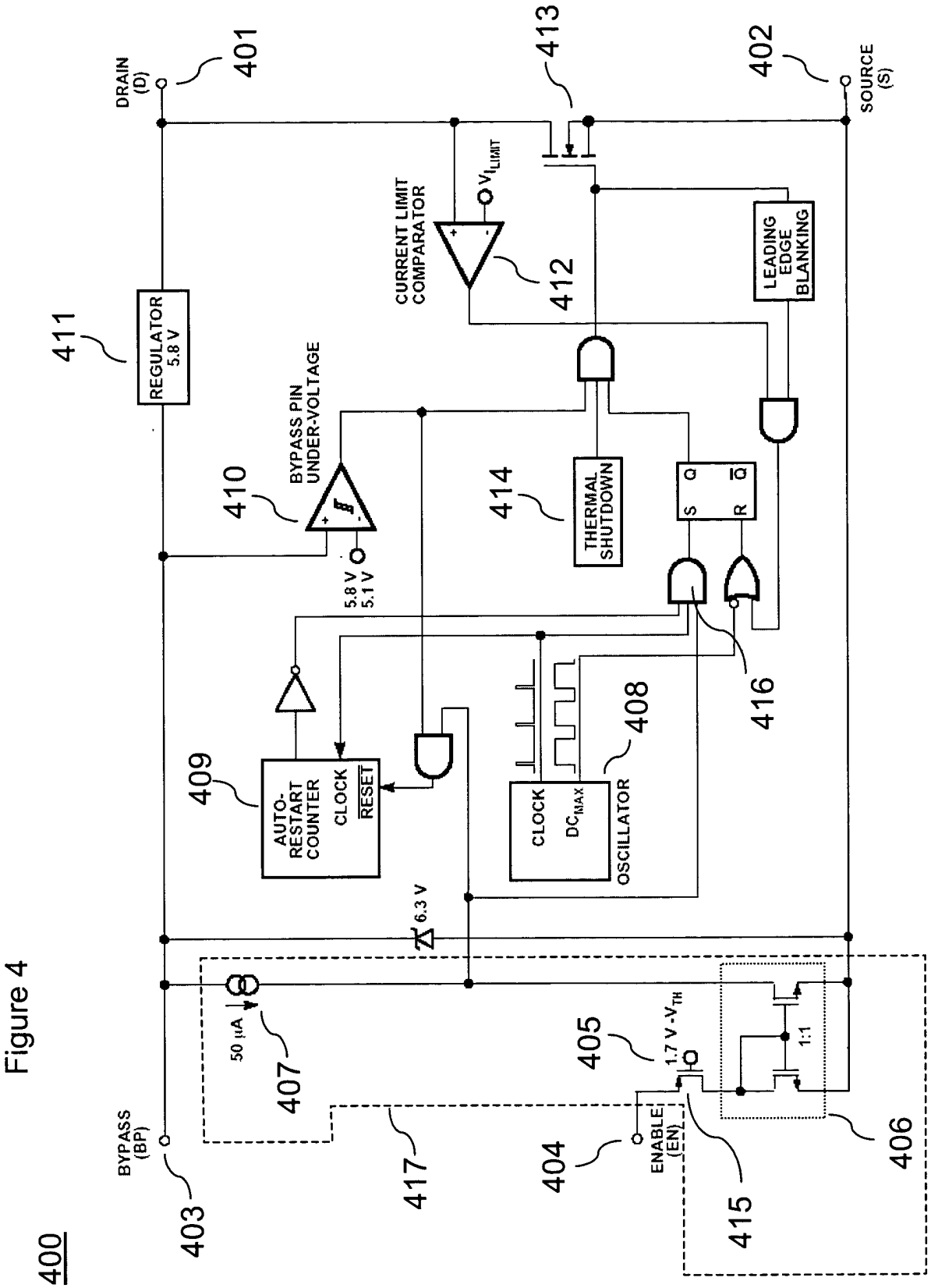

400
DRAIN (D)
401
SOURCE (S)
402
BYPASS (BP)
403
ENABLE (EN)
404
405
406
407
408
409
410
411
412
413
414
415
416
417
REGULATOR 5.8 V
CURRENT LIMIT COMPARATOR
$V_{I_{LIMIT}}$
LEADING EDGE BLANKING
BYPASS PIN UNDER-VOLTAGE
5.8 V
5.1 V
THERMAL SHUTDOWN
S
Q
R
$\overline{Q}$
AUTO-RESTART COUNTER
CLOCK
RESET
CLOCK
DC_{MAX}
OSCILLATOR
6.3 V
50 µA
1.7 V - V_{TH}
1:1

Figure 5

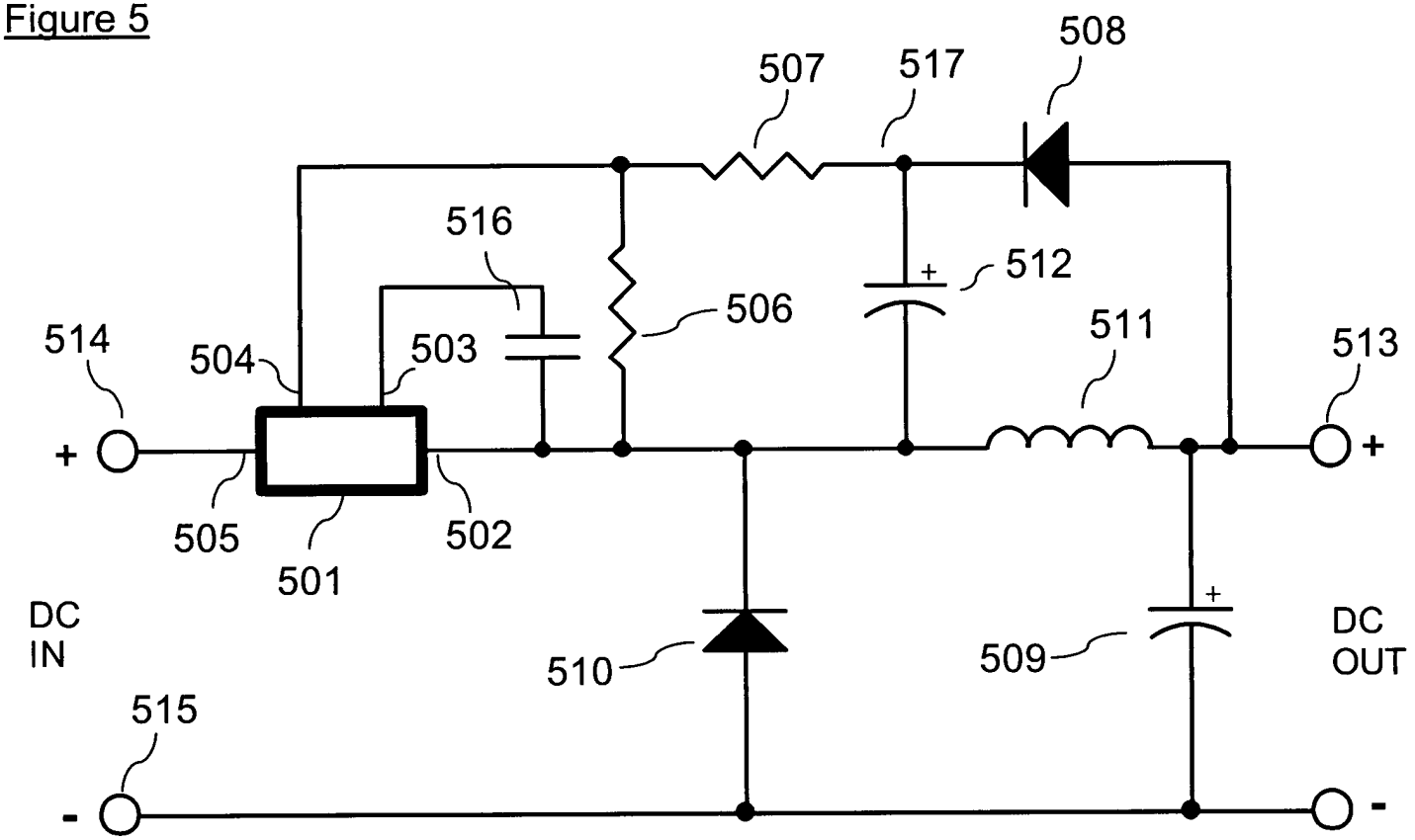

507
517
508
516
506
512
514
504
503
511
513
+
505
501
502
DC
IN
510
509
DC
OUT
515
-

Figure 6

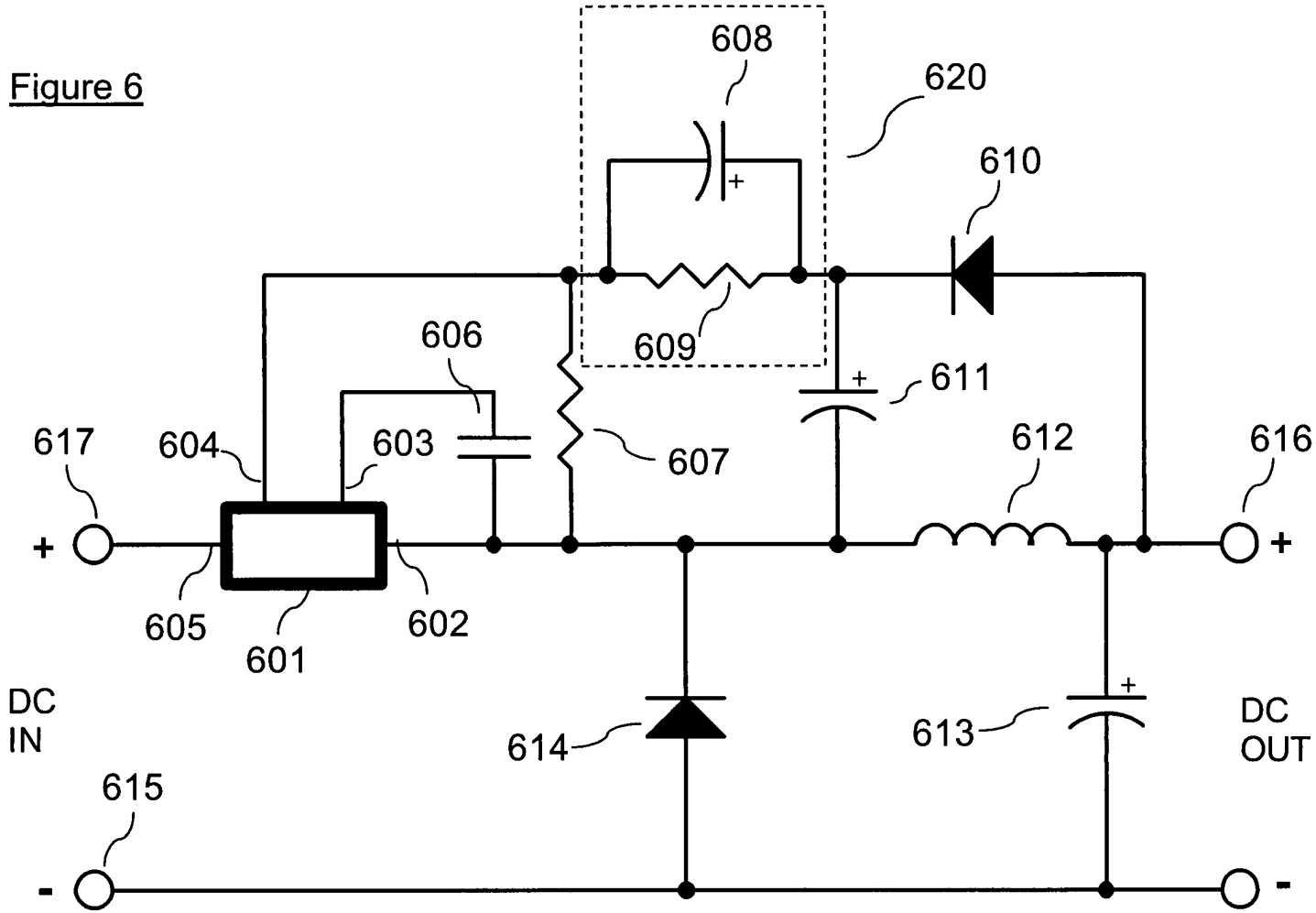

608
620
610
609
606
611
617
604
603
607
612
616
+
605
601
602
DC
IN
614
613
DC
OUT
615
-

Figure 7

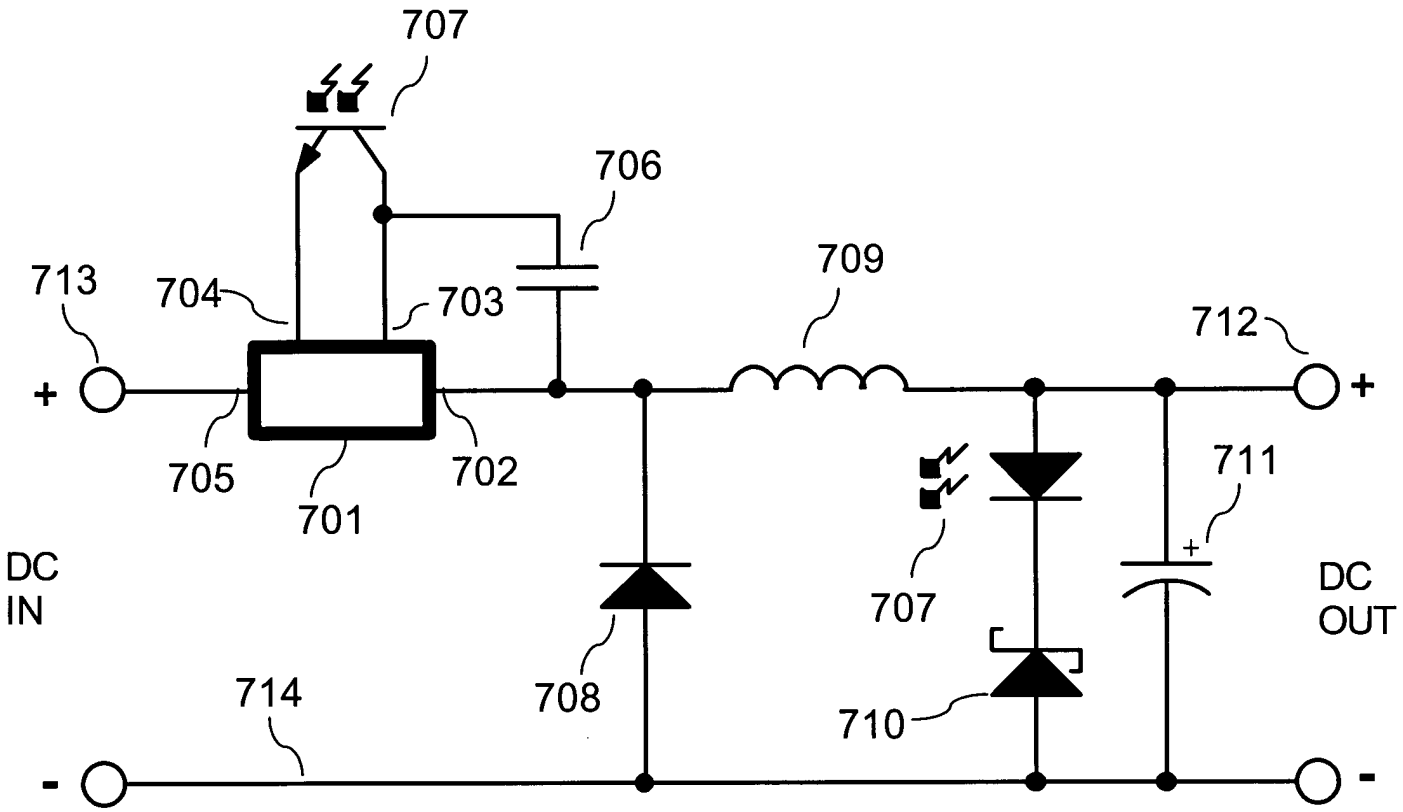
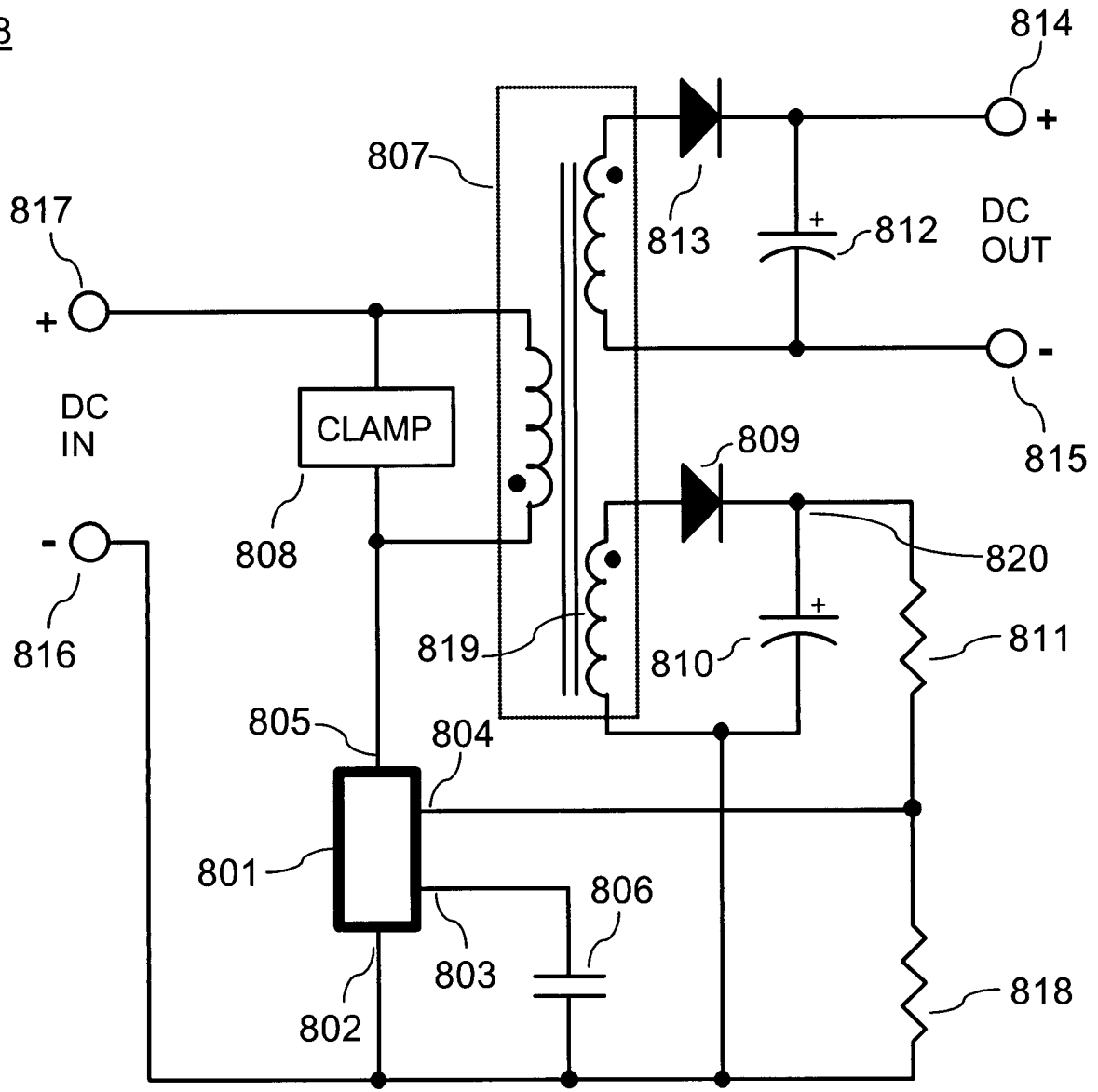

707
706
713
704
703
709
712
+
+
705
701
702
711
DC
IN
707
DC
OUT
714
708
710
-
-

Figure 8


814
+
807
817
813
812
DC
OUT
+
DC
IN
CLAMP
-
815
809
808
820
-
816
819
810
811
805
804
801
806
803
802
818

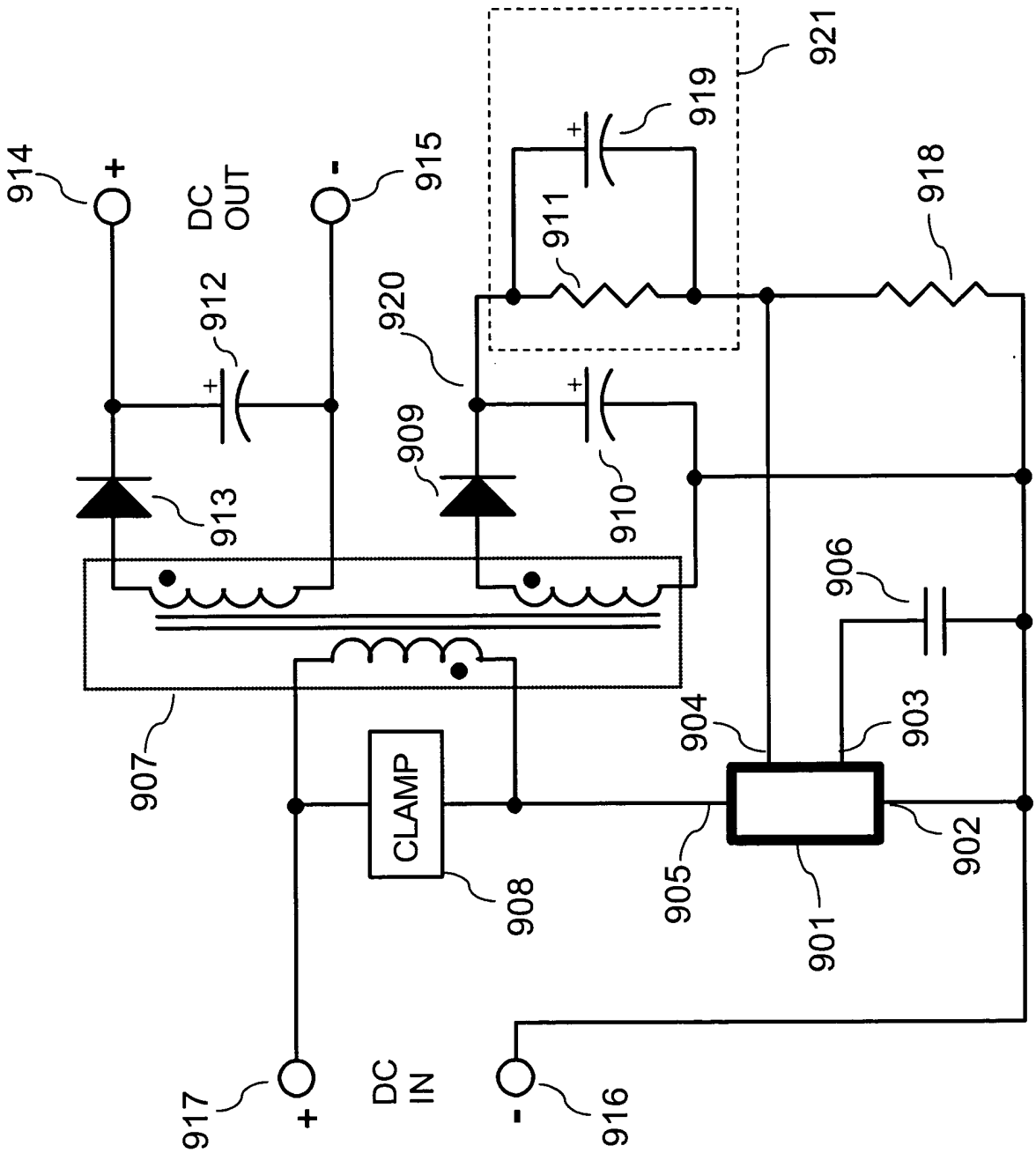

914
+
DC
OUT
-
915
912
913
909
910
911
919
921
920
918
906
907
CLAMP
908
904
903
905
902
901
917
+
DC
IN
-
916

Figure 9